

2817
#2

Docket No. 174/223

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Stjepan W. Andrasic et al.
Application No.: 10/099,707 Confirmation No.: 2004
Filed : March 13, 2002
For : VOLTAGE CONTROLLED OSCILLATOR PROGRAMMABLE
DELAY CELLS
Group Art Unit : 2817

Hon. Commissioner for Patents
P.O. Box 2327
Arlington, VA 22202

TRANSMITTAL LETTER FOR
INFORMATION DISCLOSURE STATEMENT

RECEIVED
JUN 13 2002
TC 2800 MAIL ROOM

Sir:

Transmitted herewith is an Information Disclosure Statement in the above-identified application. This Statement is submitted:

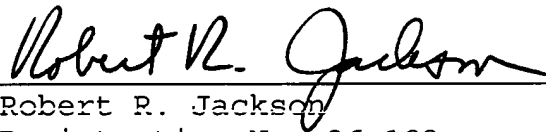
- ☒ [X] within three months of the application filing date;
- ☐ [] more than three months from the application filing date but before the mailing date of the first Office Action on the merits.

In accordance with 37 C.F.R. § 1.97, submission of this Statement requires no fee. However, if for any reason a fee is due, the Director is hereby authorized to charge payment of any fees required in connection with this

Information Disclosure Statement to Deposit Account

No. 06-1075. A duplicate copy of this letter is transmitted herewith.

Respectfully submitted,

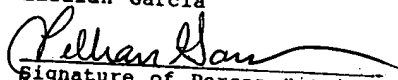


Robert R. Jackson
Registration No. 26,183
Attorney for Applicants
FISH & NEAVE
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020-1105
(212) 596-9000

I Hereby Certify that this
Correspondence is being
Deposited with the U.S.
Postal Service as First
Class Mail in an Envelope
Addressed to:
COMMISSIONER FOR
PATENTS P.O. BOX 2327
ARLINGTON, VA 22202 on

JUNE 6, 2002

Lillian Garcia


Signature of Person Signing



174/223

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Stjepan W. Andrasic et al.
Application No.: 10/099,707 Confirmation No.: 2004
Filed : March 13, 2002
For : VOLTAGE CONTROLLED OSCILLATOR
PROGRAMMABLE DELAY CELLS
Group Art Unit : 2817

Hon. Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97,
applicants wish to call the attention of the Examiner to the
following references:

U.S. patent 3,473,160 Wahlstrom

U.S. patent appln. No. 09/805,843, filed March 1, 2001
Aung et al.

"ORCA ORT82G5 1.0-1.25/2.0-2.5/3.125 Gbits/s Backplane
Interface FPSC", Preliminary Data Sheet, July 2001,
Agere Systems Inc., pp. 1-35.

"ORCA ORT8850 Field-Programmable System Chip (FPSC)
Eight Channel x 850 Mbits/s Backplane Transceiver",
Product Brief, July 2001, Agere Systems Inc., pp. 1-6.

"ORCA ORT8850 Field-Programmable System Chip (FPSC)
Eight-Channel x 850 Mbits/s Backplane Transceiver",
Data Sheet, August 2001, Agere Systems Inc, pp. 1-36.

RECEIVED
JUN 13 2002
12 2800 MAIL ROOM

These references are also listed on the attached
Form PTO-1449 (submitted in duplicate), and copies are
enclosed.

Consideration of the foregoing in relation to this
patent application is respectfully requested.

Respectfully submitted,



Robert R. Jackson
Reg. No. 26,183
Attorney for Applicants
FISH & NEAVE
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020
Tel.: (212) 596-9000

I Hereby Certify that this
Correspondence is being
Deposited with the U.S.
Postal Service as First
Class Mail in an Envelope
Addressed to:
COMMISSIONER FOR
PATENTS P.O. Box 2327
ARLINGTON, VA 22202 on

June 6, 2002
Lillian Garcia


Signature of Person Signing